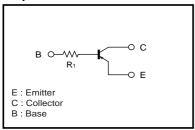
Digital transistors (built-in resistor)

DTA115TM / DTA115TE / DTA115TUA / DTA115TKA / DTA115TSA

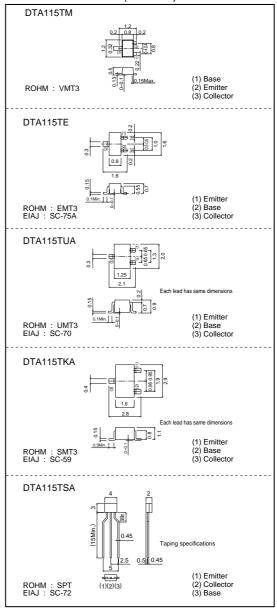
●Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors.
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input, and parasitic effects are almost completely eliminated.
- 3) Only the on / off conditions need to be set for operation, making device design easy.
- 4) Higher mounting densities can be achieved.

●Equivalent circuit



●External dimensions (Unit: mm)



● Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Collector-base voltage		Vсво	-50	V
Collector-emitter voltage		VCEO	-50	V
Emitter-base voltage		VEBO	– 5	V
Collector current		Ic	-100	mA
Collector power dissipation	DTA115TM / DTA115TE		150	mW
	DTA115TUA / DTA115TKA	Pc	200	
	DTA115TSA		300	
Junction temperature		Tj	150	°C
Storage temperature		Tstg	-55 to +150	°C

•Package, marking, and packaging specifications

Part No.	DTA115TM	DTA115TE	DTA115TUA	DTA115TKA	DTA115TSA
Package	VMT3	EMT3	UMT3	SMT3	SPT
Marking	99	99	99	99	-
Packaging code	T2L	TL	T106	T146	TP
Basic ordering unit (pieces)	8000	3000	3000	3000	5000

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	-50	_	-	V	Ic= -50μA
Collector-emitter breakdown voltage	BVceo	-50	-	-	V	Ic=-1mA
Emitter-base breakdown voltage	ВVево	-5	-	-	V	I _E = -50μA
Collector cutoff current	Ісво	-	-	-0.5	μΑ	Vcb= -50V
Emitter cutoff current	ІЕВО	-	-	-0.5	μΑ	V _{EB} = -4V
Collector-emitter saturation voltage	VCE(sat)	-	-	-0.3	V	Ic/I _B = -1mA/-0.1mA
DC current transfer ratio	hfe	100	250	600	-	Ic=-1mA , Vc==-5V
Input resistance	R ₁	70	100	130	kΩ	_
Transition frequency	f⊤	-	250	_	MHz	Vc=-10V , I=5mA , f=100MHz *

^{*}Transition frequency of the device.

•Electrical characteristics curves

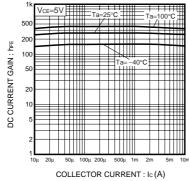


Fig.1 DC current gain vs. Collector current

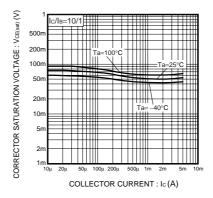


Fig.2 Collector-Emitter saturation voltage vs. Collector current

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